

Abstract

The invention provides a smart card chip having a nonvolatile system memory (ROM, flash1), a Java Card Virtual Machine implemented in the nonvolatile system memory (ROM, flash1), a nonvolatile application memory (EEPROM, flash2), a volatile working memory (RAM) and a variables memory area reserved for global variables, wherein the variables memory area is reserved in the volatile working memory (RAM). The variables memory area is preferably reserved statically. The use of the variables can be limited to system packages and optionally additionally to preloaded (ROM/EEPROM) packages.

Figure 1